Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently amended)   An electronic circuit comprising:

a plurality of sequential logic elements, each ~~one~~ of the sequential logic elements comprising:

at least one clock terminal for receiving a clock signal;

at least one input terminal for receiving an input signal;

at least one output terminal for providing an output signal;

circuitry, ~~for respective ones of the plurality of sequential logic elements,~~ for monitoring ~~respective ones of~~ said input and output signals of respective sequential logic elements to provide respective ~~analog~~ control signals in response ~~thereto~~ to said input and output signals of said respective sequential logic elements; and

means for ~~combining~~ summing said respective ~~analog~~ control signals to form a ~~continuously variable combined analog~~ summed control signal, wherein an analog level of said summed control signal is the sum of analog levels of said respective control signals, and controlling a power consumption of the electronic circuit in response to ~~an analog level of~~ said ~~continuously variable combined analog~~ summed control signal.

2. (Currently amended)   An electronic circuit as claimed in claim 1, wherein ~~it~~ said electronic circuit is capable of being controlled at a rate determined by the clock signal.

3. (Currently amended)   An electronic circuit as claimed in claim 1, wherein said electronic circuit is capable of providing information relating to future power consumption.

4. (Previously presented)   An electronic circuit as claimed in claim 1, wherein future power consumption is controllable in advance based upon past logical events.

5. (Previously presented)   An apparatus that includes an electronic circuit as claimed in claim 1.

6. (Currently amended)   A method of controlling power consumption of an electronic circuit that includes a plurality of sequential logic elements, each one of the sequential logic elements comprising:

at least one clock terminal for receiving a clock signal,

at least one input terminal for receiving an input signal, and

at least one output terminal for providing an output signal,

the method comprising the steps of:

~~for respective ones of the plurality of sequential logic elements,~~ monitoring ~~respective ones of~~ said input and output signals of respective sequential logic elements to provide respective ~~analog~~ control signals in response ~~thereto~~ to said input and output signals of said respective sequential logic elements; and

~~combining~~summing said respective ~~analog~~ control signals to form a ~~continuously variable combined analog~~ summed control signal, wherein an analog level of said summed control signal is the sum of said respective control signals, and controlling a power consumption of the electronic circuit in response to ~~an analog level of~~ said ~~continuously variable combined analog~~ summed control signal.